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Haratsch 1-4



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant(s): Erich Franz Haratsch, Harish Viswanathan
Case: 1-4
Serial No.: To Be Assigned
Filing Date: To Be Assigned
Title: Method and Apparatus for Reducing the Computational Complexity and Relaxing the Critical Path of Reduced State Sequence Estimation (RSSE) Techniques
Group: To Be Assigned
Examiner: To Be Assigned

INFORMATION DISCLOSURE STATEMENT

Assistant Commissioner of Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. §§1.56, 1.97 and 1.98, Applicant's attorney wishes to bring to the attention of the Patent and Trademark Office the following documents listed on the accompanying PTO Form 1449. A copy of each listed item is enclosed.

1. P.R. Chevillat and E. Eleftheriou, "*Decoding of Trellis-Encoded Signals in the Presence of Intersymbol Interference and Noise*", IEEE Trans. Commun., July 1989, vol. 37, pp. 669-676.
2. A. Duel-Hallen, "*Delayed Decision-Feedback Sequence Estimation*", IEEE Trans. Commun., May 1989, vol. 37, no. 5.
3. M.V. Eyuboglu and S.U.H. Qureshi, "*Reduced-State Sequence Estimation for Coded Modulation On Intersymbol Interference Channels*", IEEE JSAC., August 1989, vol. 7, pp. 989-995.
4. R. Raheli, G. Marino, and P. Castoldi, "*Per-Survivor Processing and Tentative Decisions: What is in Between?*" IEEE Trans. Commun., Feb. 1996, vol. 44, pp. 127-129.

The filing of this Information Disclosure Statement shall not be construed as a representation that a search has been made, or as an admission that the information cited is considered to be material to patentability or that no other material information exists.

Respectfully submitted,

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Date: June 4, 1999

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